

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1	6253334.pn.	USPAT	2003/09/05 16:04
2	BRS	L3	831885	ram or random access memory	USPAT	2003/09/05 16:38
3	BRS	L4	34517	(ram or random access memory) and frame\$ and (network or www) and (backplane or back plane or back-plane)	USPAT	2003/09/05 16:53
4	BRS	L5	18587	(ram or random access memory) and frame\$ and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks)	USPAT	2003/09/05 16:53
5	BRS	L6	4980	(ram or random access memory) and frame\$ and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring	USPAT	2003/09/05 16:56
6	BRS	L7	1595	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4	USPAT	2003/09/05 16:58
7	BRS	L8	1424	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4 and (fpga or field programmable array)	USPAT	2003/09/05 16:59
8	BRS	L9	947	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4 and (fpga or field programmable array) and interrupt\$4	USPAT	2003/09/05 16:59

	Type	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L10	503	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4 and (fpga or field programmable array) and (interrupt\$4 same signal)	USPAT	2003/09/05 17:00
10	BRS	L11	331	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4 and (fpga or field programmable array) and (interrupt\$4 same signal) and interval\$	USPAT	2003/09/05 17:02
11	BRS	L12	347	(ram or random access memory) and (transfer\$4 same frame\$) and (network or www) and (backplane or back plane or back-plane) and (sonet or synchronous networks) and ring and switch\$4 and (fpga or field programmable array) and (interrupt\$4 same signal) and interval\$ and control\$4	USPAT	2003/09/05 17:02